

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Patrick Larsson, et al.
Serial No.: 09/955,424
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Title: MULTI-CHANNEL SERDES RECEIVER FOR CHIP-TO-CHIP
AND BACKPLANE INTERCONNECTS AND METHOD OF
OPERATION THEREOF
Grp./A.U.: 2637
Examiner: Sam K. Ahn

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being electronically filed with United States Patent and trademark Office on:	
July 21, 2006	(Date)
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(Signature of the person signing the certificate)	

Sir:

PRELIMINARY AMENDMENT SUBMITTED WITH RCE UNDER 37 C.F.R. §1.114(c)

Before continuing examination of the above-identified application, please refer to the below arguments and amend the claims as indicated.